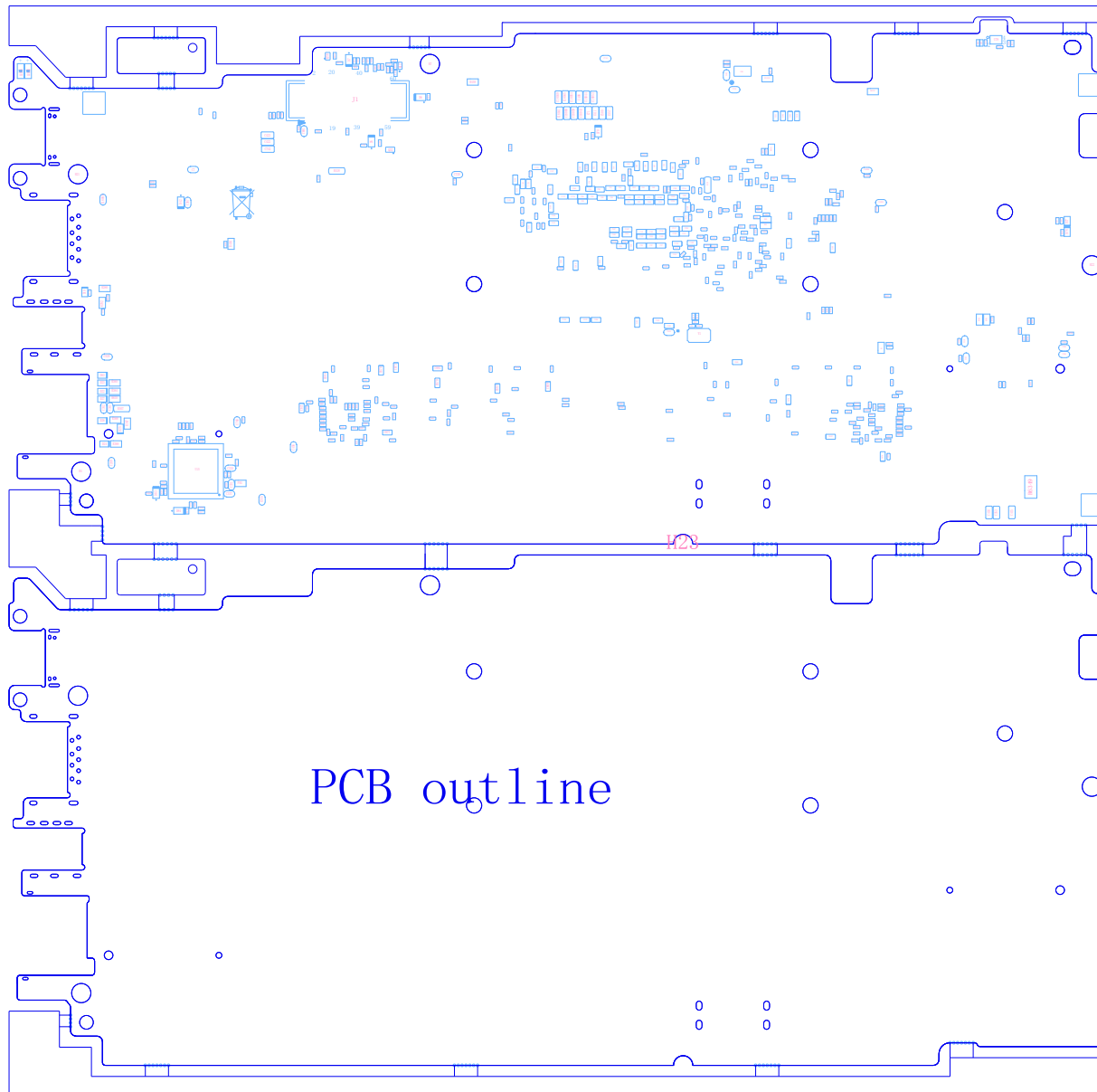


en:ltuo BCP





PCB outline